VS-400U(R) Series

Vishay Semiconductors



Standard Recovery Diodes (Stud Version), 400 A



PRODUCT SUMMARY				
I _{F(AV)}	400 A			
Package	DO-205AB (DO-9)			
Circuit configuration	Single diode			

FEATURES

- Wide current range
- High surge current capabilities
- Stud cathode and stud anode version
- Standard JEDEC[®] types
- Designed and qualified for industrial level
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

- Converters
- Power supplies
- Machine tool controls
- High power drives

MAJOR RATINGS AND CHARACTERISTICS					
PARAMETER	TEST CONDITIONS	VALUES	UNITS		
		400	A		
I _{F(AV)}	T _C	120	°C		
I _{F(RMS)}		630	A		
I _{FSM}	50 Hz	8250	٨		
	60 Hz	8640	A		
l ² t	50 Hz	340	kA ² s		
1-1	60 Hz	311	- KA ² S		
V _{RRM}	Range	800 to 1600	V		
TJ		-40 to 200	°C		

ELECTRICAL SPECIFICATIONS

Revision: 25-Nov-13

VOLTAGE RATINGS							
TYPE NUMBER	VOLTAGE CODE	V _{RRM} , MAXIMUM REPETITIVE PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} MAXIMUM AT T _J = T _J MAXIMUM mA			
	80	800	900				
VS-400U(R)	120	1200	1300	15			
	160	1600	1700				



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FORWARD CONDUCTION									
PARAMETER	SYMBOL	TEST CONDITIONS			VALUES	UNITS			
Maximum average forward current		190° conducti				180° conduction, half sine wave			A
at case temperature	I _{F(AV)}		ion, nan sine wa	ve	120	°C			
Maximum RMS forward current	I _{F(RMS)}	DC at 110 °C	case temperatui	e	630	А			
		t = 10 ms	No voltage		8250	A			
Maximum peak, one cycle forward,		t = 8.3 ms	reapplied	Sinusoidal half wave, initial T _J = T _J maximum	8640				
non-repetitive surge current	I _{FSM}	t = 10 ms	100 % V _{BBM}		6940				
		t = 8.3 ms	reapplied		7270				
Maximum I ² t for fusing	l ² t	t = 10 ms	No voltage reapplied		340	- kA ² s			
		t = 8.3 ms			311				
		t = 10 ms	100 % V _{RRM} reapplied		241				
		t = 8.3 ms			220				
Maximum I ² \sqrt{t} for fusing	l²√t	t = 0.1 to 10 ms, no voltage reapplied			3400	kA²√s			
Low level value of threshold voltage	V _{F(TO)1}	(16.7 % x π x I _{F(AV)} < I < π x I _{F(AV)}), T _J = T _J maximum			0.77	v			
High level value of threshold voltage	V _{F(TO)2}	$(I > \pi x I_{F(AV)}), T_J = T_J maximum$			0.85	v			
Low level value of forward slope resistance	r _{f1}	(16.7 % x π x I _{F(AV)} < I < π x I _{F(AV)}), T _J = T _J maximum			0.49	mΩ			
High level value of forward slope resistance	r _{f2}	$(I > \pi x I_{F(AV)}), T_J = T_J maximum$			0.49	11152			
Maximum forward voltage drop	V _{FM}	$I_{pk} = 1500 \text{ A}, T_J = T_J \text{ maximum}, t_p = 10 \text{ ms sinusoidal wave}$			1.62	V			

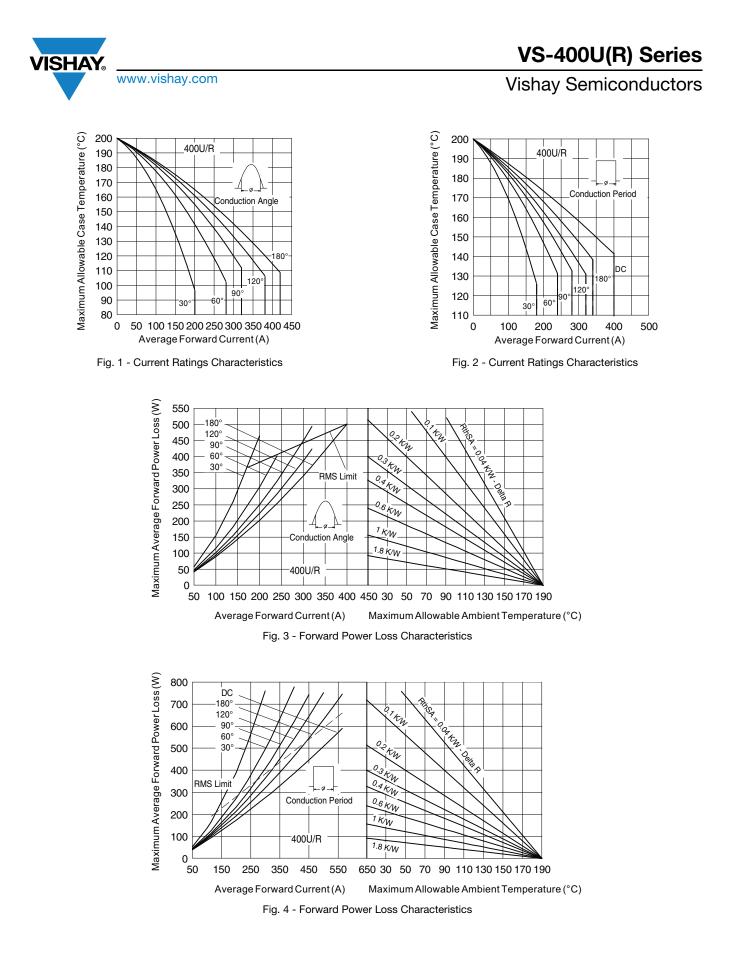
THERMAL AND MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction operating and storage temperature range	T _J , T _{Stg}		-40 to 200	°C	
Maximum thermal resistance, junction to case	R _{thJC}	R _{thJC} DC operation 0.1		K/W	
Maximum thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth, flat and greased 0		r./ vv	
Maximum allowed mounting torque ± 10 %		Not lubricated threads 27		N·m	
Approximate weight			250	g	
Case style		See dimensions - link at the end of datasheet DO-205AB (DO-9)		3 (DO-9)	

CONDUCTION ANGLE	SINUSOIDAL CONDUCTION	RECTANGULAR CONDUCTION	TEST CONDITIONS	UNITS			
180°	0.020	0.013					
120°	0.023	0.023					
90°	0.029	0.031	$T_J = T_J maximum$	K/W			
60°	0.042	0.044					
30°	0.073	0.074					

Note

The table above shows the increment of thermal resistance R_{thJC} when devices operate at different conduction angles than DC

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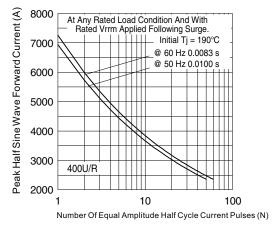


Fig. 5 - Maximum Non-Repetitive Surge Current

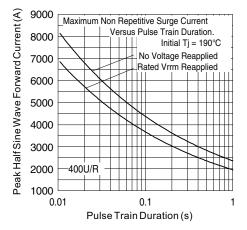


Fig. 6 - Maximum Non-Repetitive Surge Current

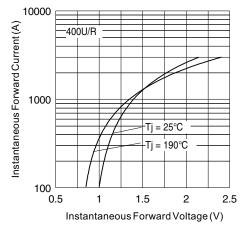
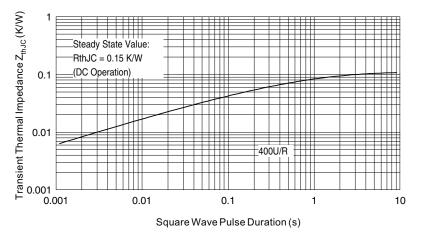


Fig. 7 - Forward Voltage Drop Characteristics





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ORDERING INFORMATION TABLE

								
Device code	VS-	40	0	U	R	160	D	
	1	2	3	4	5	6	$\overline{7}$	
	1	- Vish	ay Sem	iconduc	tors pro	duct		
	2 - 40 = Essential part number							
	3.	0 = 3	Standard	d recove	ery devi	ce		
	4 ·	- U =	Stud no	rmal pol	arity (ca	athode t	o stud)	
	5.	• No	one = St	ud norm	nal polar	ity (cath	node to	stud)
		• R	= Stud r	everse	polarity	(anode	to stud)	
	6	- Volta	age cod	e x 10 =	V _{RRM} (see Vol	ltage Ra	itings table)
	7	Diffu	ised dio	de				

Note: For metric device M16 x 1.5 contact factory

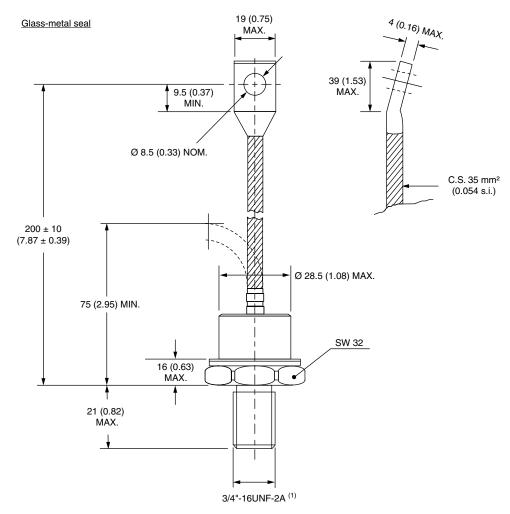
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95339			

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DO-205AB (DO-9) for 400U(R) Series

DIMENSIONS in millimeters (inches)



Note

• For metric device: M16 x 1.5 contact factory



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